

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (cancelled).
2. (previously presented): A two-dimensional active-matrix type light modulation device comprising:
 - a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;
 - a plurality of counter electrodes;
 - a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in accordance with an applied voltage between said pixel electrode and said counter electrode; and
 - a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit writes data to said ferroelectric gate field-effect transistors in order of a row,
 - wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel,
 - wherein the single ferroelectric gate field effect transistor has its gate directly connected to a first data line.
3. (previously presented): The two-dimensional active-matrix type light modulation

device as set forth in claim 2, wherein said drive circuit writes data to all of said pixels and then applies a voltage for driving said light modulating layer between said counter electrode and said pixel electrode in common for all pixels.

4. (previously presented): A two-dimensional active-matrix type light modulation device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in accordance with an applied voltage between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit changes a ferroelectric gate of said ferroelectric gate field-effect transistor to a first polarization state and then writes data in accordance with input of data, changing said first polarization state to a second polarization state, or said first polarization state, in accordance with said input of data,

wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel,

wherein the single ferroelectric gate field effect transistor has its gate directly connected to a first data line.

5. (previously presented): A two-dimensional active-matrix type light modulation device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in accordance with an applied voltage between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit performs row selection with a gate electrode of said ferroelectric gate field-effect transistor and writes data with a source electrode and drain electrode of said ferroelectric gate field-effect transistor and a substrate electrode or back-surface electrode of said ferroelectric gate field-effect transistor,

wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel,

wherein the single ferroelectric gate field effect transistor has its gate directly connected to a first data line.

6. (previously presented): A two-dimensional active-matrix type light modulation device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in

accordance with an applied voltage between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes wherein said drive circuit performs modulation by binary static drive,

wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel,

wherein the single ferroelectric gate field effect transistor has its gate directly connected to a first data line.

7. (cancelled).

8. (previously presented): A two-dimensional active-matrix type light-emitting device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light-emitting layers, each light-emitting layer being interposed between said pixel electrode and said counter electrode for emitting light in accordance with current flowing through the light-emitting layer between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit writes data to said ferroelectric gate field-effect transistors in order of a row,

wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate

field-effect transistor per pixel,

wherein the single ferroelectric gate field effect transistor has its gate directly connected to a first data line.

9. (previously presented): The two-dimensional active-matrix type light-emitting device as set forth in claim 8, wherein said drive circuit writes data to all of said pixels and then allows a current for driving said light-emitting layer to pass through said counter electrode and said pixel electrode in common for all pixels.

10. (previously presented): A two-dimensional active-matrix type light-emitting device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light-emitting layers, each light-emitting layer being interposed between said pixel electrode and said counter electrode for emitting light in accordance with current flowing through the light-emitting layer between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit changes a ferroelectric gate of said ferroelectric gate field effect transistor to a first polarization state and then writes data in accordance with input of data, changing said first polarization state to a second polarization state, or said first polarization state, in accordance with said input of data,

wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate

field-effect transistor per pixel,

wherein the single ferroelectric gate field effect transistor has its gate directly connected to a first data line.

11. (previously presented): A two-dimensional active-matrix type light-emitting device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light-emitting layers, each light-emitting layer being interposed between said pixel electrode and said counter electrode for emitting light in accordance with current flowing through the light-emitting layer between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit performs row selection with a gate electrode of said ferroelectric gate field-effect transistor and writes data with a source electrode and drain electrode of said ferroelectric gate field-effect transistor and a substrate electrode or back-surface electrode of said ferroelectric gate field-effect transistor,

wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel,

wherein the single ferroelectric gate field effect transistor has its gate directly connected to a first data line.

12. (previously presented): The two dimensional active-matrix type light modulation device according to claim 2, wherein said drive circuit changes a ferroelectric gate of said ferroelectric gate field-effect transistor to a first polarization state and then writes data in accordance with input of data, changing said first polarization state to a second polarization state, or so said first polarization state is held in accordance with said input of data.

13. (previously presented): The two dimensional active-matrix type light modulation device according to claim 2, wherein aid drive circuit performs row selection with a gate electrode of said ferroelectric gate field-effect transistor and writes data with a source electrode and drain electrode of said ferroelectric gate field-effect transistor and a substrate electrode and drain electrode of said ferroelectric gate field-effect transistor and a substrate electrode or back-surface electrode of said ferroelectric gate field-effect transistor.

14. (previously presented): The two dimensional active-matrix type light modulation device according to any one of claims 2, 4 and 5, wherein said drive circuit performs modulation by binary static drive.

15. (previously presented): The two-dimensional active-matrix type light-emitting device as set forth in claim 8, wherein said drive circuit changes a ferroelectric gate of said ferroelectric gate field effect transistor to a first polarization state and then writes data in accordance with input of data, changing said first polarization state to a second polarization state, or said first polarization state is held in accordance with said input of data.

16. (previously presented): The two-dimensional active-matrix type light-emitting device as set forth in claim 8 or 10, wherein said drive circuit performs row selection with a gate electrode of said ferroelectric gate field-effect transistor and writes data with a source electrode

and drain electrode of said ferroelectric gate field-effect transistor and a substrate electrode or back-surface electrode of said ferroelectric gate field-effect transistor.

17. (cancelled).

18. (previously presented): The two-dimensional active-matrix type light modulation device as set forth in claim 13, wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel.

19. (previously presented): The two-dimensional active-matrix type light modulation device as set forth in claim 14, wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel.

20. (previously presented): The two-dimensional active-matrix type light-emitting device as set forth in any one of claims 8-11 and 15, wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel.

21. (previously presented): The two-dimensional active-matrix type light-emitting device as set forth in claim 16, wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel.

22. (previously presented): The two-dimensional active-matrix type light modulation device as set forth in any one of claims 2-6 and 12, wherein the ferroelectric gate field-effect transistor consists of a single type of semiconductor selected from one of an n-type and a p-type semiconductor.

23. (previously presented): The two-dimensional active-matrix type light modulation device as set forth in claim 13, wherein the ferroelectric gate field-effect transistor consists of a single type of semiconductor selected from one of an n-type and a p-type semiconductor.

24. (previously presented): The two-dimensional active-matrix type light modulation device as set forth in claim 14, wherein the ferroelectric gate field-effect transistor consists of a single type of semiconductor selected from one of an n-type and a p-type semiconductor.

25. (previously presented): The two-dimensional active-matrix type light-emitting device as set forth in any one of claims 8-11 and 15, wherein the ferroelectric gate field-effect transistor consists of a single type of semiconductor selected from one of an n-type and a p-type semiconductor.

26. (previously presented): The two-dimensional active-matrix type light-emitting device as set forth in claim 16, wherein the ferroelectric gate field-effect transistor consists of a single type of semiconductor selected from one of an n-type and a p-type semiconductor.

27. (previously presented): A two-dimensional active-matrix type light modulation device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in accordance with an applied voltage between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein a gate of the ferroelectric field effect transistor is directly connected to line selecting one of said rows.

28. (previously presented): A two-dimensional active-matrix type light modulation

device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in accordance with an applied voltage between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit writes data to said ferroelectric gate field-effect transistors in order of a row,

wherein the ferroelectric gate field-effect transistor comprises a first ferroelectric gate field-effect transistor per pixel, wherein the first ferroelectric gate field effect transistor per pixel has its gate directly connected to a first data line, and said device further comprising a second ferroelectric gate field-effect transistor per pixel, said second ferroelectric gate field-effect transistor having its gate directly connected to a second data line.

29. (previously presented): A two-dimensional active-matrix type light modulation device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in

accordance with an applied voltage between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit changes a ferroelectric gate of said ferroelectric gate field-effect transistor to a first polarization state and then writes data in accordance with input of data, changing said first polarization state to a second polarization state, or said first polarization state, in accordance with said input of data, wherein the ferroelectric gate field-effect transistor comprises a first ferroelectric gate field-effect transistor per pixel, wherein the first ferroelectric gate field effect transistor per pixel has its gate directly connected to a first data line, and said device further comprising a second ferroelectric gate field-effect transistor per pixel, said second ferroelectric gate field-effect transistor having its gate directly connected to a second data line.

30. (previously presented): A two-dimensional active-matrix type light modulation device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in accordance with an applied voltage between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit performs row selection with a gate electrode of said ferroelectric gate field-effect transistor and writes data with a source electrode

and drain electrode of said ferroelectric gate field-effect transistor and a substrate electrode or back-surface electrode of said ferroelectric gate field-effect transistor,

wherein the ferroelectric gate field-effect transistor comprises a first ferroelectric gate field-effect transistor per pixel, wherein the first ferroelectric gate field effect transistor per pixel has its gate directly connected to a first data line, and said device further comprising a second ferroelectric gate field-effect transistor per pixel, said second ferroelectric gate field-effect transistor having its gate directly connected to a second data line.

31. (previously presented): A two-dimensional active-matrix type light modulation device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light modulating layers, each light modulating layer being interposed between said pixel electrode and said counter electrode for modulating light incident thereon in accordance with an applied voltage between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes wherein said drive circuit performs modulation by binary static drive,

wherein the ferroelectric gate field-effect transistor comprises a first ferroelectric gate field-effect transistor per pixel, wherein the first ferroelectric gate field effect transistor per pixel has its gate directly connected to a first data line, and said device further comprising a second

ferroelectric gate field-effect transistor per pixel, said second ferroelectric gate field-effect transistor having its gate directly connected to a second data line.

32. (previously presented): A two-dimensional active-matrix type light-emitting device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light-emitting layers, each light-emitting layer being interposed between said pixel electrode and said counter electrode for emitting light in accordance with current flowing through the light-emitting layer between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit writes data to said ferroelectric gate field-effect transistors in order of a row,

wherein the ferroelectric gate field-effect transistor comprises a first ferroelectric gate field-effect transistor per pixel, wherein the first ferroelectric gate field effect transistor per pixel has its gate directly connected to a first data line, and said device further comprising a second ferroelectric gate field-effect transistor per pixel, said second ferroelectric gate field-effect transistor having its gate directly connected to a second data line.

33. (previously presented): A two-dimensional active-matrix type light-emitting device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix

consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light-emitting layers, each light-emitting layer being interposed between said pixel electrode and said counter electrode for emitting light in accordance with current flowing through the light-emitting layer between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit changes a ferroelectric gate of said ferroelectric gate field effect transistor to a first polarization state and then writes data in accordance with input of data, changing said first polarization state to a second polarization state, or said first polarization state, in accordance with said input of data,

wherein the ferroelectric gate field-effect transistor comprises a first ferroelectric gate field-effect transistor per pixel, wherein the first ferroelectric gate field effect transistor per pixel has its gate directly connected to a first data line, and said device further comprising a second ferroelectric gate field-effect transistor per pixel, said second ferroelectric gate field-effect transistor having its gate directly connected to a second data line.

34. (previously presented): A two-dimensional active-matrix type light-emitting device comprising:

a plurality of pixel electrodes arranged in the form of a two-dimensional matrix consisting of rows and columns;

a plurality of counter electrodes;

a plurality of light-emitting layers, each light-emitting layer being interposed between

said pixel electrode and said counter electrode for emitting light in accordance with current flowing through the light-emitting layer between said pixel electrode and said counter electrode; and

a drive circuit constituted by ferroelectric gate field-effect transistors respectively connected to said pixel electrodes, wherein said drive circuit performs row selection with a gate electrode of said ferroelectric gate field-effect transistor and writes data with a source electrode and drain electrode of said ferroelectric gate field-effect transistor and a substrate electrode or back-surface electrode of said ferroelectric gate field-effect transistor,

wherein the ferroelectric gate field-effect transistor comprises a single ferroelectric gate field-effect transistor per pixel, wherein the first ferroelectric gate field effect transistor per pixel has its gate directly connected to a first data line, and said device further comprising a second ferroelectric gate field-effect transistor per pixel, said second ferroelectric gate field-effect transistor having its gate directly connected to a second data line.

35. - 41. (canceled).

42. (new). The device of claim 2, wherein the first data line comprises a row selection line.

43. (new). The device of claim 28, wherein the first data line comprises a first row selection line and the second data line comprises a second row selection line.